

PTO/SB/08A (10-01)

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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	
				Filing Date	
				First Named Inventor	Philip E. MAY
				Art Unit	
				Examiner Name	
Sheet	1	of	1	Attorney Docket Number	CML00770D

U.S. PATENT DOCUMENTS

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Examiner Signature _____ **Date Considered** _____

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**INFORMATION DISCLOSURE
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Sheet

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of

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Complete if Known

Application Number	10/652,134
Filing Date	3/29/2003
First Named Inventor	May et al.
Art Unit	2185
Examiner Name	Daniel H. Pa

Sheet

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of

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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

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Attorney Docket Number	CML00770D

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U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear.	T ⁴
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		Number - Kind Code ² (if known)			
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W		4,128,880	Dec-78	Cray Jr.	
W		4,744,043	May-88	Kloker	
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<i>R</i>		5,717,947	Dec-98	Gallup et al.	
<i>M</i>		5,719,998	Feb-98	Ku et al.	
<i>M</i>		5,764,787	Jun-98	Nickerson	
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<i>M</i>		20050053012	Mar-05	Moyer	
<i>M</i>		20050055534	Mar-05	Moyer	
<i>M</i>		20050055543	Mar-05	Moyer	
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Art Unit	2185
Examiner Name	Daniel H. Pan
Attorney Docket Number	CML00770D

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>g</i>		LAM, M., Software Pipelining: An Effective Scheduling Technique for VLIW Machines, Proceedings of the SIGPLAN '88 Conference on Programming Language Design and Implementation, Atlanta, Georgia, June 22-24, 1988, pp. 318-328	
<i>h</i>		LEE, T. et al., A Transformation-Based Method for Loop Folding, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 13, No. 4, April 1994, pp. 439-450	
<i>n</i>		KAVI, K.M. et al., A Formal Definition of Data Flow Graph Models, IEEE Transactions on Computers, Vol C-35, No. 11, Nov 1986, pp. 940-948	
<i>r</i>		COOPER, K.D. et al., Efficient Computation of Flow Insensitive Interprocedural Summary Information, SIGPLAN Notices, Vol. 19, No. 6, June 1984, pp. 247-258	
<i>m</i>		STRONG, H.R., Vector Execution of Flow Graphs, Journal of the Association for Computing Machinery, Vol 39, No. 1, January 1983, pp. 186-196	
<i>r</i>		AIKEN, A. et al., Resource-Constrained Software Pipelining, IEEE Transactions on Parallel and Distributed Systems, Vol. 6, No. 12, December 1995, pp. 1248-1270	
<i>r</i>		WULF, WILLIAM A. "Evaluation of the VM Architecture," Proceedings of the 19 th Annual International Symposium on Computer Architecture: 1992, pp. 382-390, ACM 0-89791-509-7/92/0005/0382.	
<i>r</i>		TALLA, D., Architectural techniques to Accelerate Multimedia Applications on General-Purpose Processors, Dissertation, August 2001, pp. 94-125	
<i>r</i>		TALLA, D., Bottlenecks in Multimedia Processing with SIMD Style Extensions and Architectural Enhancements, IEEE Transactions of Computers, August 2003, pp. 1015-1031, Vol. 62, No. 8, IEEE	

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